

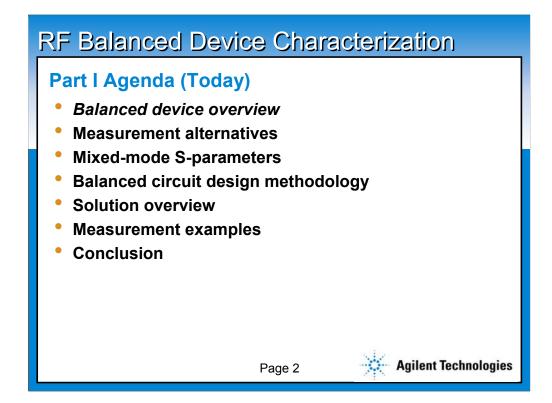
RF Balanced Device Characterization

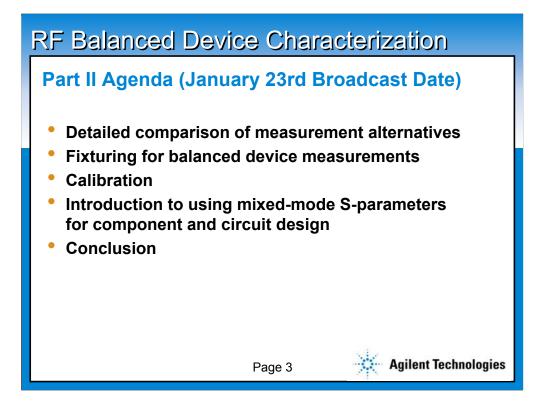
Part 1 – January 14, 2003

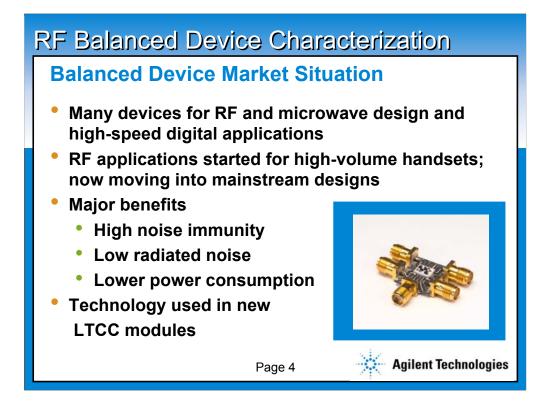
Part 2 – January 23, 2003

Presented By:

Greg Amorese David Ballo







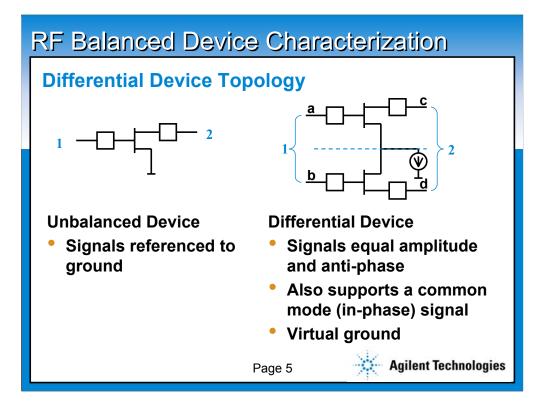
Balanced circuits have been used for many years because of their desirable performance characteristics. They have been mostly used in lower frequency analog circuitry and digital devices, and much less so in RF and microwave applications.

One benefit of differential circuits is that they have good immunity from many sources of noise such as that from power supplies, adjacent circuitry, and other external sources that are coupled either electrically or electromagnetically. These noise sources tend to couple in the common-mode, and therefore cancel in differential mode.

Well-balanced circuits not only have reduced noise susceptibility, they also will have lower radiation of the signals those circuits are carrying (i.e. reduced EMI radiation).

Cancellation also occurs at even-harmonic frequencies since signals that are antiphase at the fundamental frequency are in-phase at the even harmonics.

The quality of the virtual ground in a differential circuit is independent of the physical ground path. Therefore, differential devices can tolerate poor RF grounds better than unbalanced devices.

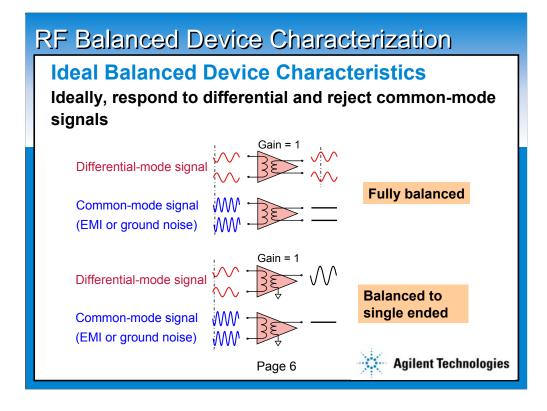


An unbalanced, or single-ended, device has all of its signals referenced to a common ground potential.

A balanced device, by comparison, is composed of two nominally identical halves. Practically speaking, the signals on each side of the device can have any relative amplitude and phase relationship, but they can be decomposed into a differentialmode (anti-phase) component, and a common-mode (in-phase) component.

A balanced circuit operating in common-mode has no performance advantages over a single-ended circuit. The advantages of this topology come from operating the device in differential mode.

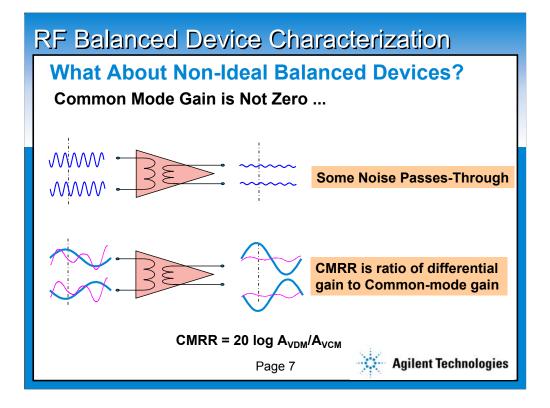
When a device is driven differentially, a virtual ground is established along its axis of symmetry. At the virtual ground, the potential at the operating frequency does not change with time regardless of the signal amplitude.



Differential signals are 180 deg. out of phase

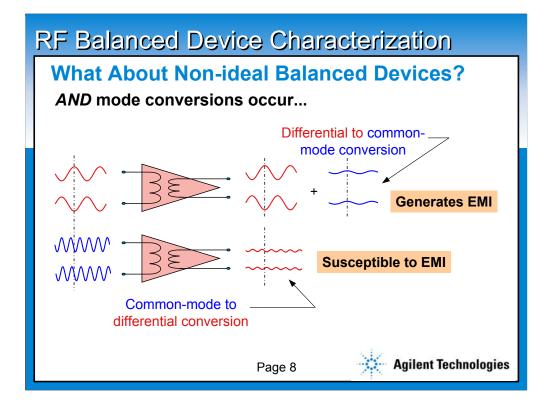
Common mode signals are in phase

Differential input is desired mode of operation



The previous slide showed the operation of an ideal balanced device. But designing high performance balanced components/devices and circuits is difficult and all devices will have some non-ideal behavior.

One of the key issues is the degree of balance of the device and how our DUT may affect the whole circuits degree of balance. As the degree of balance becomes worse, the device will perform what is called "mode conversion". If a device converts some of its incoming differential signal to common mode on its output, then the system (whole circuit) will generate EMI radiation. If a device converts some of its incoming common mode signal (typically noise) to differential on its output, that reduces the systems noise immunity.



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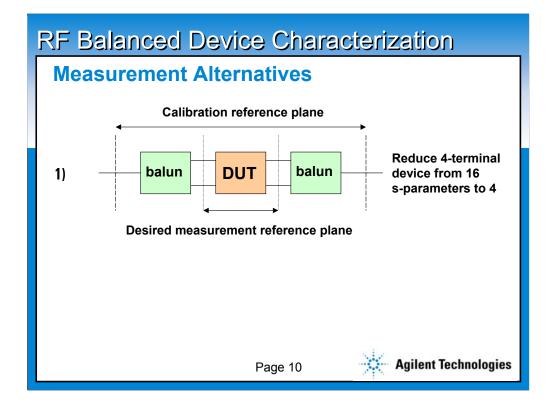
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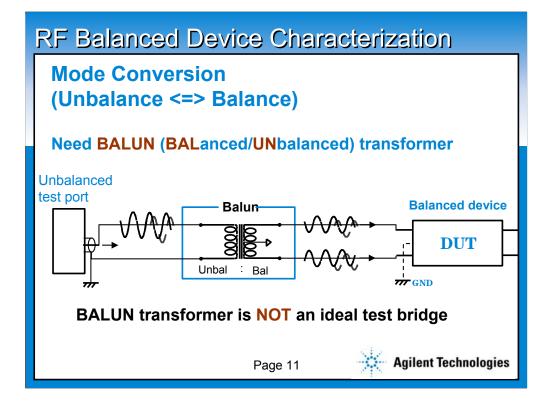
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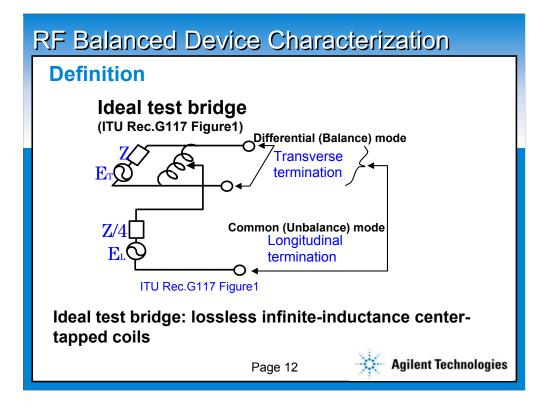
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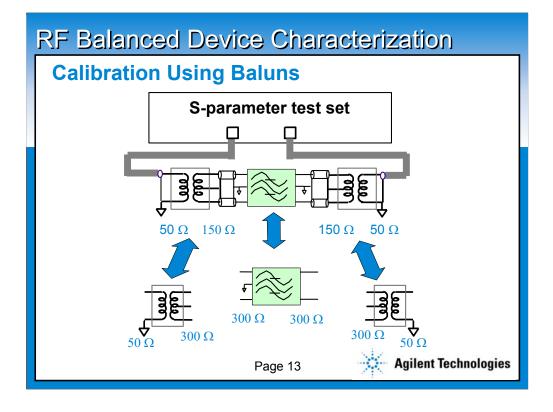
The first alternative (and historically most common) for measuring balanced devices is to convert each balanced port to a single-ended port using a balun, and measure that network on a single-ended VNA. One disadvantage to this approach is that it is inaccurate because the reference plane of the calibration is at the single-ended test port of the VNA, while the desired measurement reference plane is at the balanced port of the DUT. The balun in between is not ideal and will degrade the accuracy of the measurement. The other disadvantage is that this approach is not comprehensive since, at best, it can only portray the pure differential mode of operation, not the other three modes.



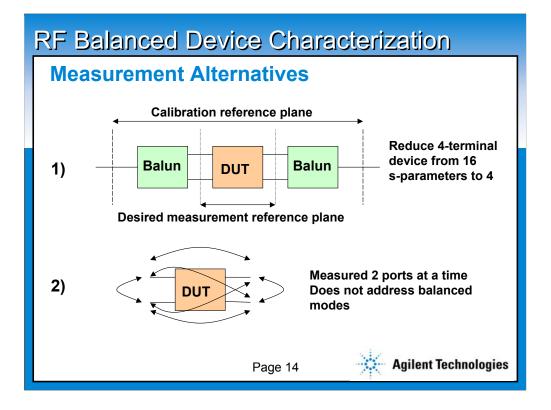
It is important to note the difference between a balun and ideal test bridge. For example, you cannot look at the common mode performance of the device using a balun since no common mode signals are allowed through the balun due to it's characteristics. A balun also looks like an infinite impedance to the device under test which will cause some measurement degradation because the device has a nominal impedance.



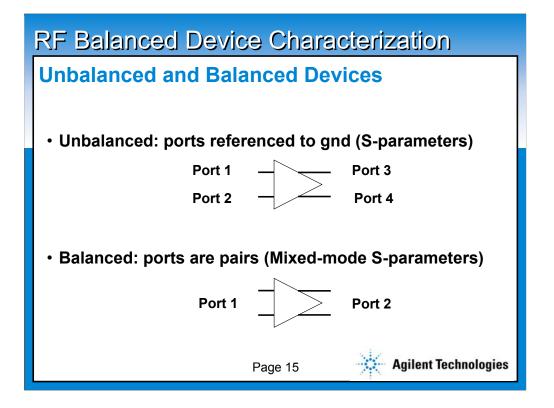
If we could specify the method of it would be using an ideal test bridge. The test bridge provides measurement capabilities for differential and common mode performance also known as mixed-mode parameters. In reality a physical structure such as this is very difficult to realize and even more difficult to perform a calibration on.



Another issue when using baluns is that we need many baluns (in theory 1 or 2 for each D UT impedance that we expect to measure). So we need many baluns. But we also have to calibrate our system and the affect of these non-ideal interfaces to our DUT's. Since no certified balanced standards exist (except at very low frequencies), we cannot directly do a calibration after the balun. The typical solution used is to do a traceable calibration at the standard connectors (e.g. SMA) and then perform a normalization by using a through (typically connecting the 2 baluns back-to-back. But a normalization has many limits on what errors can be corrected for. For example, the phase and amplitude imbalance of each of the baluns cannot be determined even though those can cause very major erros in the measurement of our DUT.

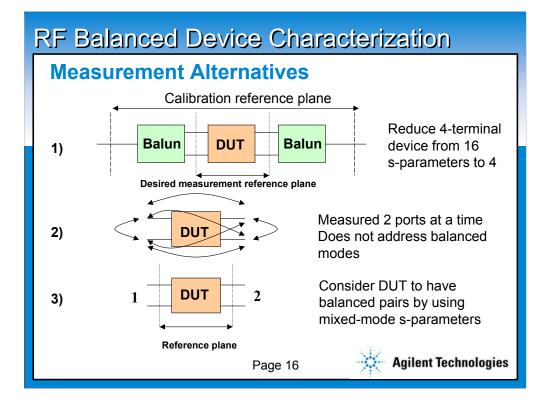


The second approach is to measure the balanced device as a single-ended multiport device. This can be a very time consuming process since multiple two port measurements are needed to fully characterize the device. In addition, it can be misleading since the single-ended data may not give a representative indication of the performance of the device when it operates in one of its balanced modes.



The second approach is to measure the balanced device as a single-ended multiport device. This can be a very time consuming process since multiple two port measurements are needed to fully characterize the device. In addition, it can be misleading since the single-ended data may not give a representative indication of the performance of the device when it operates in one of its balanced modes.

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The third method that is preferred for its accuracy, completeness, and ease of interpretation is to characterize the DUT using mixed-mode s-parameters such as measured on the Agilent Differential & Multiport Measurement Systems. In this example we treat the 4-terminal device as having only 2 ports but each of those ports are balanced ports (potentially carrying both common mode and differential mode signals). When we use this approach we can define the behavior of our device using "mixed-mode s-parameters".

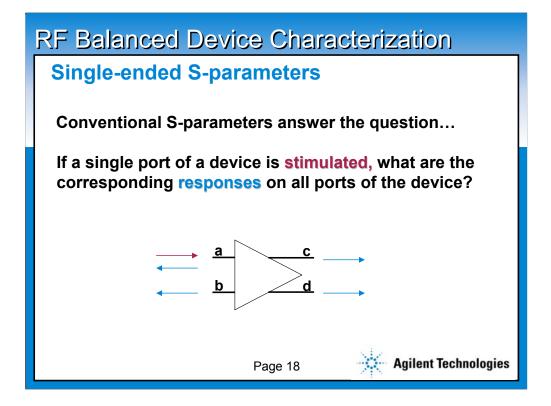
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Let's compare what is meant by single-ended and mixed-mode s-parameters. Recall that with conventional single-ended s-parameters we are describing the performance of a device when it is stimulated on a single port, and the corresponding responses are observed on all of the ports.

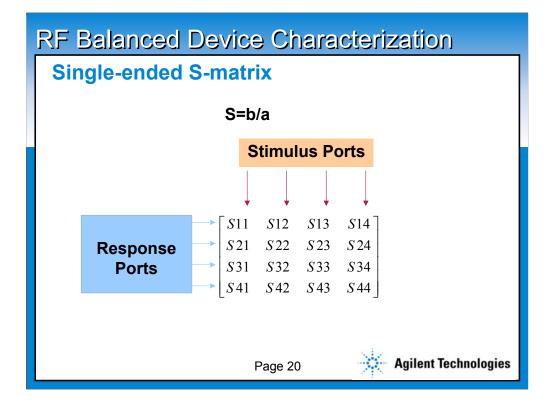
RF Balanced Device Characterization
Single-ended S-parameter Review
Single-ended 4-port Voltage Vn Port 1 — Port 3 Current In Port 2 — Port 4 Impedance Z _n = V _n ⁺ /I _n ⁺
Normalized power waves
stimulus $an = \frac{1}{2 \cdot \sqrt{\operatorname{Re} \{Zn\}}} (Vn + In \cdot Zn)$
response $bn = \frac{1}{2 \cdot \sqrt{\operatorname{Re} \{Zn\}}} (Vn - In \cdot Zn)$
S=b/a Page 19 Agilent Technologies

We have looked at the intuitive description of mixed-mode s-parameters. Now let's look at a more mathematical description.

For a single-ended device, RF voltages and currents relative to a common ground can be defined at each terminal of the device. From these we can also define an impedance from the positive-going waves.

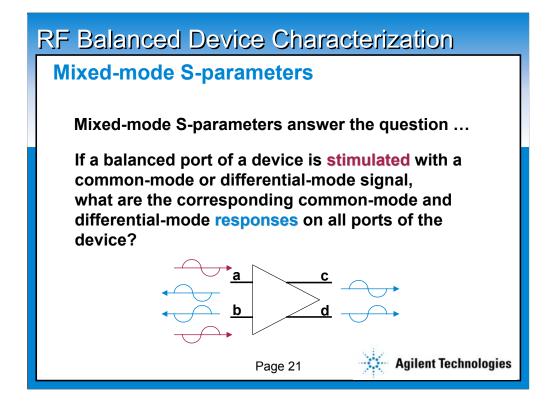
From the voltage, current, and impedance definitions, normalized power waves can be defined in stimulus and response. Stimulus power waves are defined as propagating into the DUT, and response power waves propagate away from the DUT.

The s-parameters are ratios of a response to a stimulus normalized power wave.

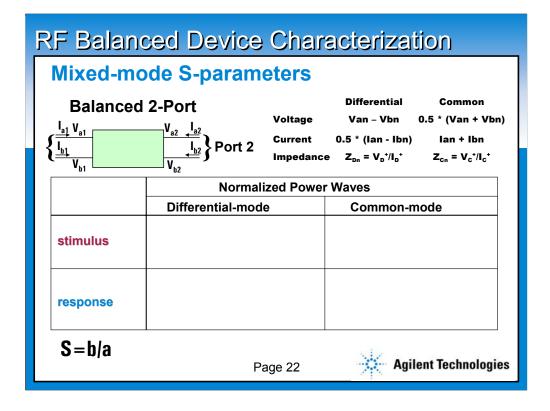


An s-parameter is defined as the ratio of two normalized power waves: the response divided by the stimulus. A full s-matrix describes every possible combination of a response divided by a stimulus.

The matrix is arranged in such a way that each column represents a particular stimulus condition, and each row represents a particular response condition.

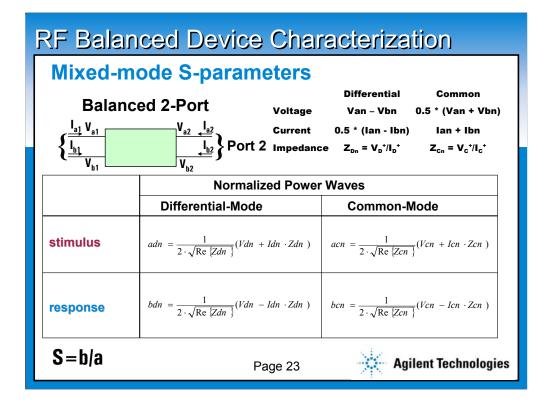


The mixed-mode s-parameters concept is similar to the single ended (traditional) s-parameters, except that instead of stimulating a single terminal of the DUT, with a balanced device we consider pairs of terminals to be stimulated in either a differential (anti-phase) or a common (in-phase) mode. With mixed-mode s-parameters we are asking, with a differential mode stimulus on a balanced port, what are the corresponding differential and common mode responses on all of the device ports? Likewise for a common mode stimulus, what are the differential and common mode responses?



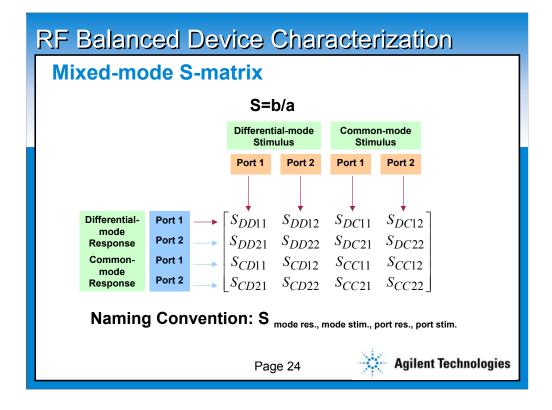
As with the single-ended case, we can also define normalized power waves on the ports of a balanced device. In this case they are mode-specific. The differential and common-mode voltages and currents defined earlier can be used for this, resulting in normalized power waves having the exact same form as the singleended case. Only the definitions of "voltage" and "current" are changed.

Mathematically, the differences between conventional single-ended s-parameters and mixed--mode s-parameters are few. Both are defined as ratios of normalized power waves.



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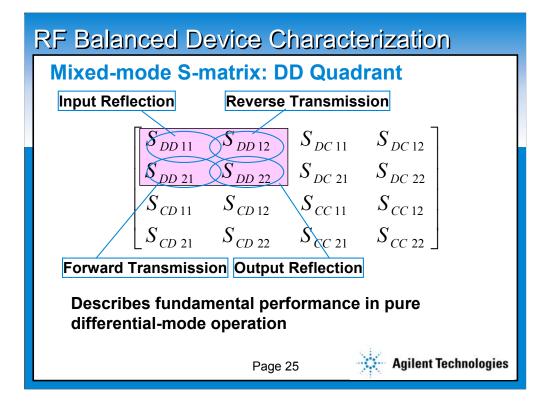
Again we can take a ratio of all of the possible combinations of response over stimulus for the differential and common-mode normalized power waves to calculate the mixed-mode s-parameters.

A mixed-mode s-matrix can be organized in a similar way to the single-ended smatrix, where each column represents a different stimulus condition, and each row represents a different response condition.

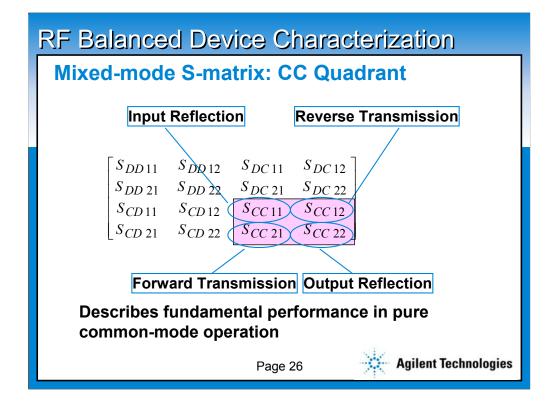
Unlike the single-ended example, though, in the mixed-mode s-matrix we are not only considering the port, we are also considering the mode of the signal at each port.

The naming convention for the mixed-mode s-parameters must include mode information as well as port information. Therefore, the first two subscripts describe the mode of the response and stimulus, respectively, and the next two subscripts describe the ports of the response and stimulus.

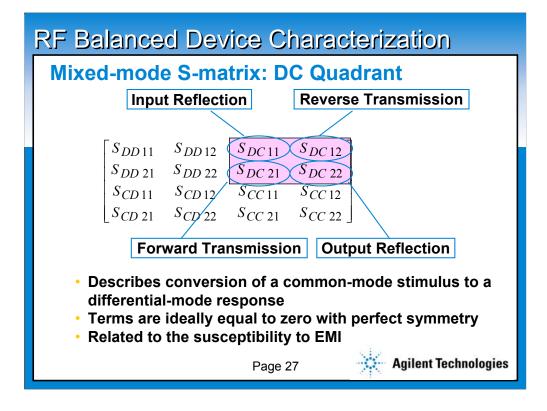
The mixed-mode matrix fully describes the linear performance of a balanced twoport network. To understand the information contained in the mixed-mode smatrix, it is helpful to examine each of its four modes of operation independently by dividing this matrix into four quadrants.



For a device with two balanced ports, the quadrant in the upper left corner of the mixed-mode s-matrix describes the performance with a differential stimulus and differential response. When the performance of the device is isolated to this specific mode, these four parameters describe the input and output reflections, and the forward and reverse transmissions much in the same way a 2-port s-matrix describes the performance of a single-ended device.



For a device with two balanced ports, the quadrant in the lower right corner of the mixed-mode s-matrix describes the performance with a common-mode stimulus and a common-mode response. When the performance of the device is isolated to this specific mode, these four parameters describe the input and output reflections, and the forward and reverse transmissions.



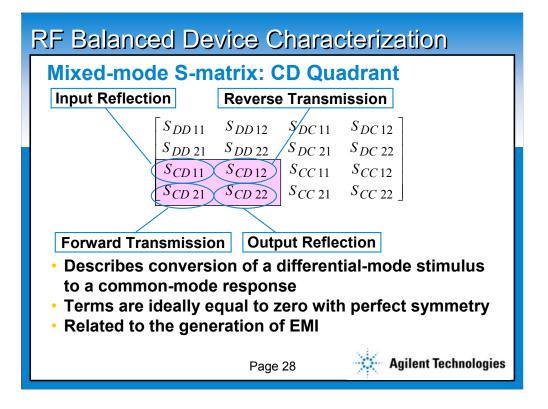
Finally, the parameters in the upper right corner describe the differential response of a device to a common-mode stimulus. Again, there are reflection parameters on each port, and transmission parameters in each direction.

In an ideal balanced device that is perfectly symmetrical, there will be no conversion from common mode to differential mode. In that case, all of these terms will be equal to zero. As the device becomes asymmetrical, these terms become larger. Therefore, the mode conversion terms provide a measure of device symmetry.

Why is mode conversion important?

All of the performance benefits of differential circuits assume that the device is symmetrical. The benefits become diminished as the device becomes more asymmetrical.

Where differential to common mode conversion is related to the generation of EMI in a balanced device, the common to differential terms are related to the susceptibility of a device to EMI. Common-mode noise, for example, can become converted to differential mode and degrade the signal-to-noise ratio of the system.

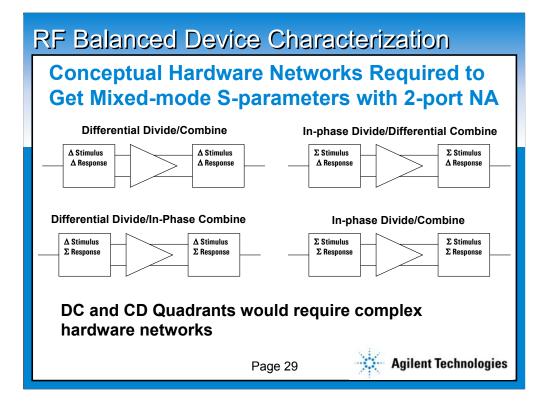


The parameters in the lower left corner describe the common-mode response of a device to a differential stimulus. As with the other modes, there are reflection parameters on each port, and transmission parameters in each direction.

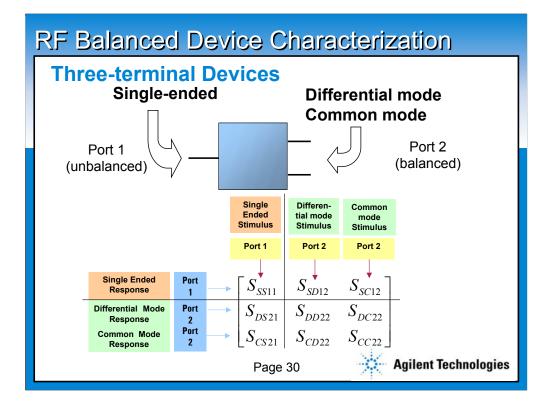
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The same benefits of symmetry apply to this mode as discussed in the CD quadrant.

Differential to common mode conversion is even related to the generation of EMI in a balanced device. The differential mode stimulus becomes converted to common mode, and appears on a ground return. From there it can be radiated as if from an antenna.



In theory we could try to measure mixed-mode s-parameters by using a standard 2-port NA and special measuring circuits that would perform the signal conversion necessary based on the definitions of mixed-mode s-parameters. In practice making those special measuring circuits work well is almost impossible.



The balanced device that was examined until now has had two balanced ports. A simple extension of the mixed-mode concept can be applied to devices having a combination of balanced and single-ended ports. In this scenario, we need to consider differential and common modes on the balanced ports, and one mode on the single-ended port.

The s-matrix for such a device is again arranged with the stimulus conditions in the columns, and the response conditions in the rows. Notice that two columns and two rows describe each balanced port, and one column and one row describe each single-ended port.

In this case the four parameters in the lower right corner describe the four types of reflection that are possible on a balanced port, the single parameter in the upper left describes the reflection on the single-ended port, and the other four parameters describe the differential and common mode transmission characteristics in the forward and revere directions.

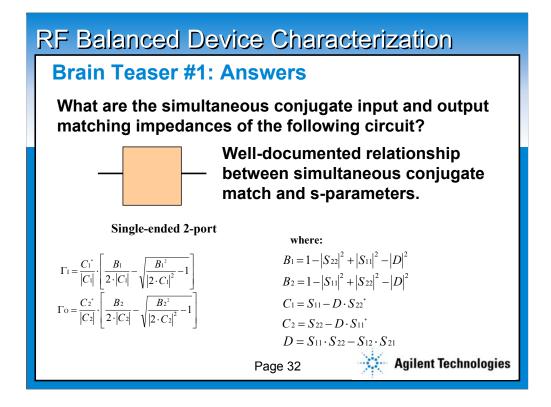
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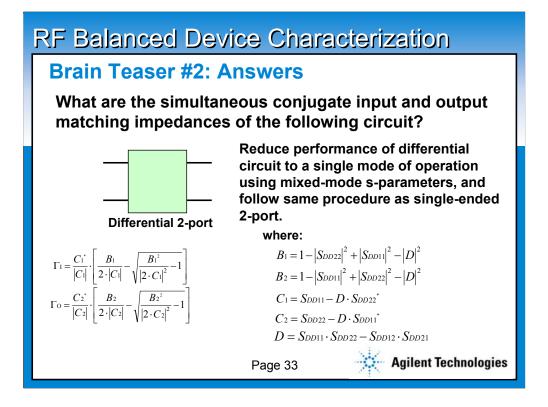
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Although you may not have the answer on the tip of your tongue, there is a welldocumented relationship between the s-parameters of a single-ended two-port and the simultaneous conjugate match. This relationship can be found in many reference books and articles.

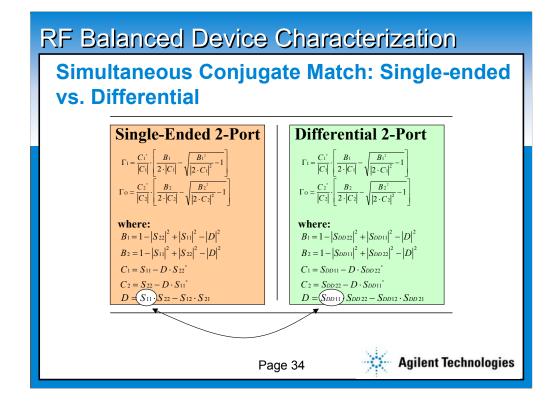


This a place where many designers get stumped today because they look at this device as a four-port and know that the concept of simultaneous conjugate match does not exist for a device with more than two ports.

Earlier we showed how the mixed-mode s-parameters are defined mathematically, and how similar they are to single-ended s-parameters. A very powerful property of the mixed-mode s-parameters is that if a balanced device is isolated to a specific mode, the resulting two port parameters can be used exactly the way single-ended two-port s-parameters are used.

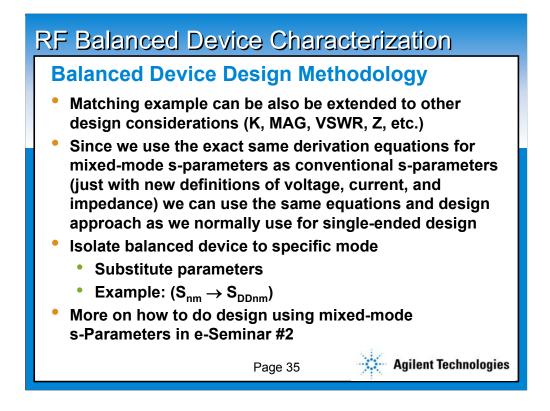
Even though our device has four *terminals*, it has only two *ports*, and we have defined it to be operating in a differential mode.

Therefore, if we isolate its operation to a differential mode, we know its performance from the upper-left quadrant of the mixed-mode s-matrix. This 2-by-2 sub-matrix can, therefore, be used the same way a 2-by-2 s-matrix is used for single-ended devices. The formulas are exactly the same, we simply substitute parameters.



Comparing these calculations one more time shows the similarities. They are identical except for a parameter substitution.

Using this technique, designing a differential device becomes as straightforward as designing a single-ended device.



The same formulas that relate single-ended two-port s-parameters to other performance parameters, such as stability factor, maximum available gain, VSWR, port impedance, and others, can also be applied to differential devices.

The reason, again, is that they have been mathematically derived in a similar manner as the single-ended parameters.

The implications are very significant. This allows the same design procedures that are used in the design of single-ended devices to be applied to the design of balanced devices. The key is to isolate the device to a specific mode of operation.

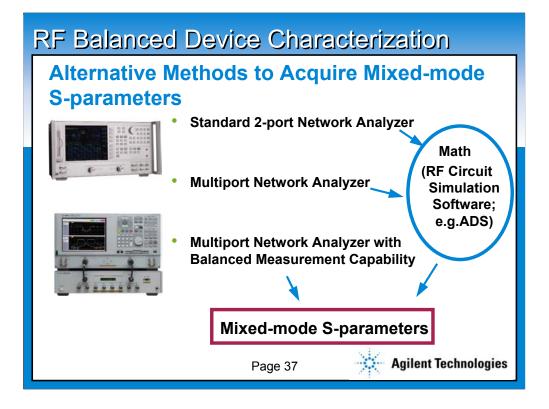
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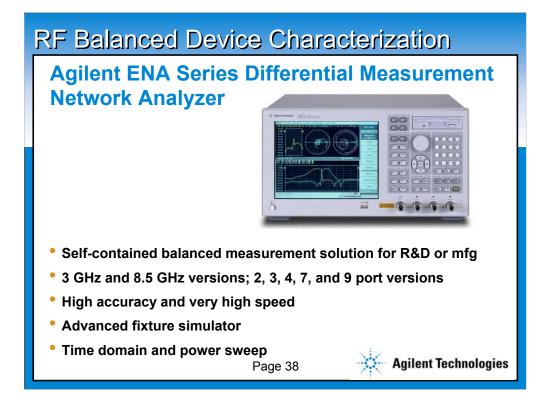
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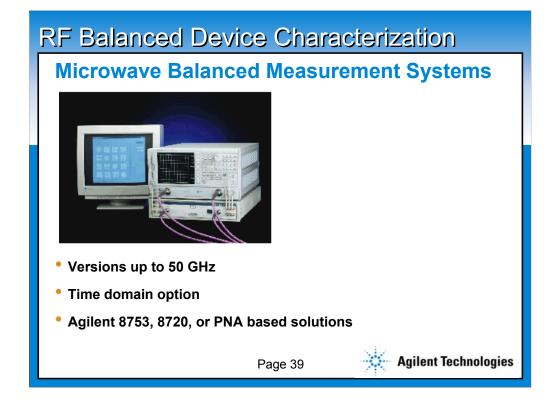


The primary measurement equipment recommended for LTCC device measurements is the Network Analyzer (NA). The NA's have an internal signal source and a tuned receiver. These 2 elements are coupled allowing extremely accurate, high dynamic range swept amplitude and phase measurements for devices that have high rejection ratios like the SAW devices contained in an LTCC. Whether the NA is a 2-port or multiport version, it has S-parameter test sets and sophisticated calibration capabilities to get very useful, accurate characteristics.

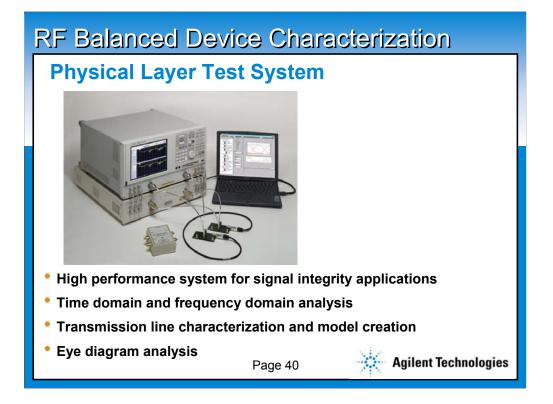
A "standard" NA has 2 ports for measuring both transmission and reflective responses. But these 2 ports only allow connection to 2 terminals of our DUT at a time. Since LTCC devices have more than 2 terminals (ports) then a version of a NA with more than 2 ports would be desireable. Such NA's exist and are termed Multiport NA's. If we have an LTCC with many ports but all ports are coaxial (unbalanced) then we can easily use a "normal" Multiport NA. If our LTCC device has balanced ports, then we could use a "normal" multiport NA along with some mathematical data manipulation, but it would be much more convenient to use a Multiport NA that already includes Balanced measurement capability.



Agilent has also developed a new high speed series of multiport network analyzers. This is called the ENA series. This is an integrated multiport and balanced measurement solution (I.e. the instrument directly displays mixed-mode sparameters). These network analyzers use a receiver per channel architecture resulting in very high speed high accuracy measurements.



These photos show 6GHz and 20GHz systems that work with the 8753 and 8720 VNA's, respectively. This is a true 4-port measurement system with true 4-port error correction. Designing the systems in this way is essential for obtaining accurate mixed-mode s-parameter data.



Agilent has also developed a high speed multiport test system for testing LTCC components. This test system is based on the PNA series network analyzer platform. The PNA platform offers many benefits for high speed testing including very fast sweep, excellent RF performance (such as dynamic range), and integrated automation capabilities. These capabilities along with an external test set and integrated multiport application provide the user a multiport test system for high speed multiport testing.

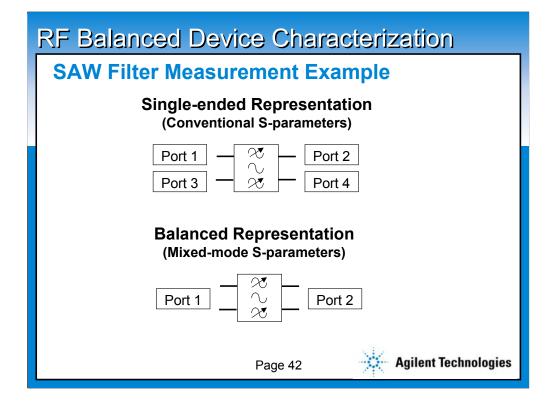
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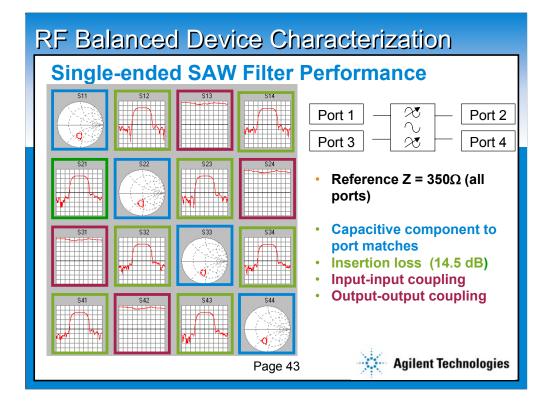
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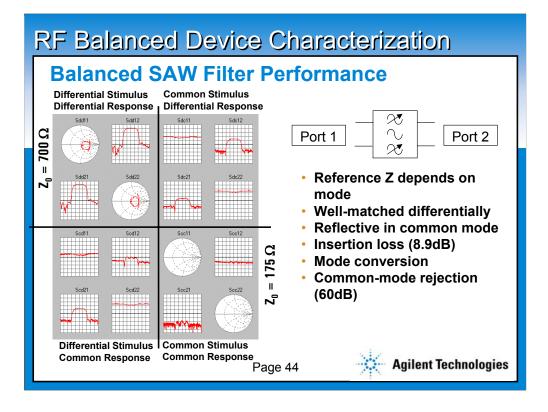
To illustrate the approach to balanced device characterization, measured data on a balanced SAW filter now will be discussed. We will look at both the singleended data and the mixed-mode data to illustrate the importance of analyzing balanced devices in the intended mode of operation.



The single-ended 4-port data is shown here. The device was designed to have differential port impedance of 700 ohms. Therefore, the single-ended has been normalized to 350 ohms on each port. The single-ended port matches are shown along the diagonal. They all look similar, and exhibit a clear capacitive component to the impedance.

The parameters below the diagonal show the 6 transmission paths through the device. Among these are 4 that pass through the filter element, and 2 that describe the isolation between the balanced input and output pairs.

The six terms above the diagonal are the same 6 transmission paths in the opposite direction. Since this is a passive, reciprocal device, each parameter above the diagonal is equal to a parameter below the diagonal.

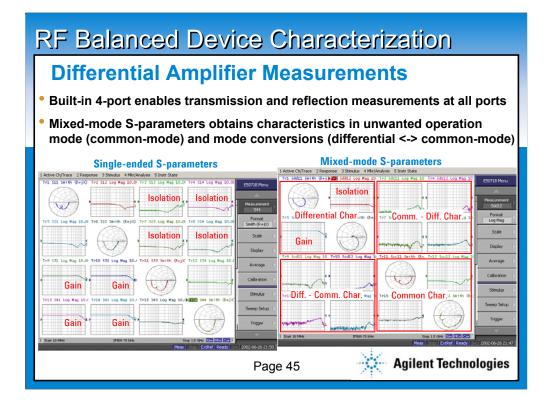


This slide show the mixed-mode s-parameter data. The four quadrants describe the performance in each of its modes of operation.

The DD quadrant in the upper left corner shows the performance in a pure differential mode. This data is normalized to a reference impedance of 700 ohms differential. The input and output reflections now show a well-matched device. A differential device does not see the capacitance that a single-ended source sees. It is tempting to assume that the differential impedance is twice the single-ended impedance. In general this is not true as this example clearly shows. The transmission responses now give a much better indication of the performance of the device in its intended operating mode.

The CC quadrant is normalized to a reference impedance of 175 ohms common. In this mode the ports are very reflective, and very little of the signal is transmitted through the device (-70dB). A CMRR of 60dB can be calculated by dividing the differential-mode gain to the common-mode gain.

The CD and DC quadrants show the mode-conversion. These parameters are at least -25dB in-band. Whether this is acceptable depends on the system in which the device is used.



Here is a simple example to illustrate the importance of using mixed-mode sparameters rather than single-ended s-parameters to characterize balanced devices.

Consider a simple differential amplifier. First we can examine the input matches on each of the four terminals and see that the device is matched on all four ports. We can also see that the gain from port 1 to port 3, and from port 2 to port 4 are each about 8dB.

When the device is driven differentially, we can see that the device is also matched on both its input and output ports, and that the differential-mode gain is also 8dB.

Finally we can see that the common-mode performance looks exactly like the differential-mode performance, with matched ports and a gain of 8dB. This device, therefore, has no common-mode rejection.

The similarity between the single-ended, differential-mode, anc common-mode performance of this device is a special case that results from the two halves of the device being perfectly isolated from each other by the ideal ground.

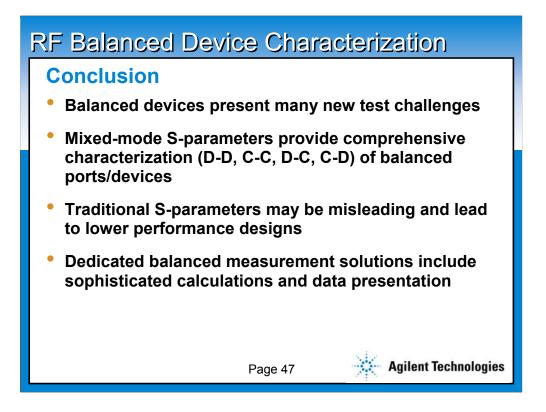
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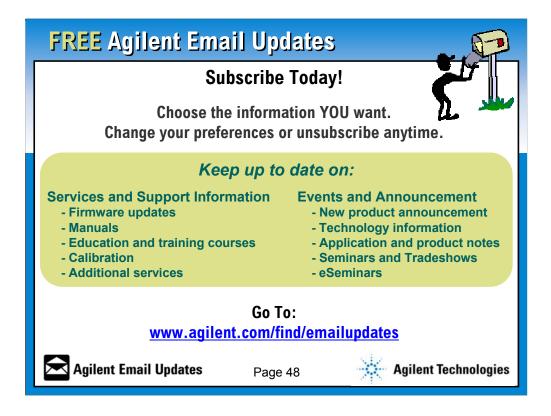


In conclusion, balanced devices are being increasingly used because of the many performance benefits they offer. The representation described here shows how the performance of balanced devices can be represented.

This technique provides much better accuracy than other commonly used alternate techniques, and does to require a new infrastructure of balanced calibration standards or balanced interconnect components.

The mixed-mode s-parameters comprehensively describe the performance of a DUT as a balanced device, and are not misleading like examining the single-ended s-parameters of a balanced device can be.

Measuring each operating mode of a balanced device provides very good insight into the impact that that device will have on the performance of the system.



In a moment we will begin with the Q&A but 1^{st,} for those of you who have enjoyed today's broadcast, Agilent Technologies is offering a new service that allows you to receive customized Email Updates. Each month you'll receive information on:

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